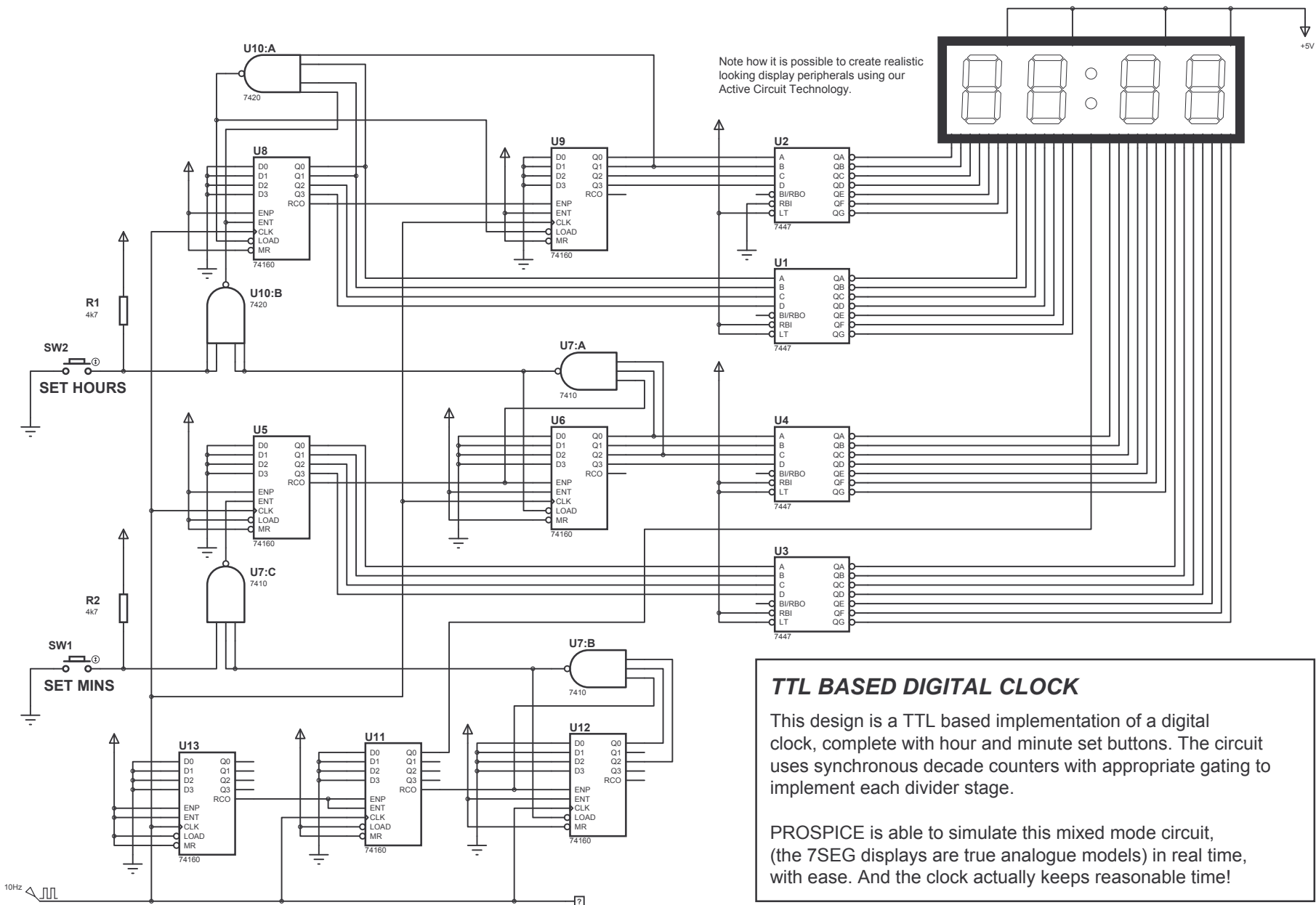




TTL BASED DIGITAL CLOCK

This design is a TTL based implementation of a digital clock, complete with hour and minute set buttons. The circuit uses synchronous decade counters with appropriate gating to implement each divider stage.

PROSPICE is able to simulate this mixed mode circuit, (the 7SEG displays are true analogue models) in real time, with ease. And the clock actually keeps reasonable time!